

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning on page 6, line 12 with the following replacement paragraph.

Returning to Figure 1, the inventive time/space switching component with multiple functionality accordingly preferably comprises 32 memory devices SM0 through SM31 with respectively 2304 memory cells K0 through K2303 each, in which the information of a complete time frame-- as described above -- can be deposited. The memory cells K0 through K2303 preferably comprise a data width of 10 bits, with 8 bits comprising the payload data and 2 bits comprising check data.